

ABSTRACT:

In a first embodiment of a transceiver according to the invention for a serial data bus, means (1) for error management are provided which supply an error signal when the data bus lines (2, 3) are active and when the receiving line simultaneously signalizes an inactive bus, said error signal having the effect that the transceiver no longer acts actively on the data bus. In a second embodiment of the transceiver according to the invention, means (1) for error management are provided in the transceiver, which means comprise a timer circuit which triggers an error signal when the transmission line is active for a longer period than a predetermined time interval, which error signal is cancelled only when the transmission line signalizes an inactive bus and the receiving line signalizes an active bus.

Figure

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